PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
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Minoru OKAMOTO	:	Confirmation Number: 4892
	:	
Application No.: 10/787,166	:	Group Art Unit: 2825
	:	
Filed: February 27, 2004	:	Examiner: SIEK, VUTHE
	:	
For: SYSTEM LSI DESIGN SUPPORT APPARATUS AND A SYSTEM LSI DESIGN SUPPORT METHOD		

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.
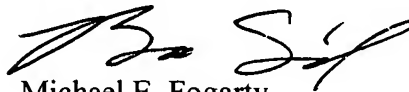
Each reference was first cited in a corresponding foreign application search report or office action and its relevance discussed therein. A copy of the search report or office action, is attached for the Examiner's information.

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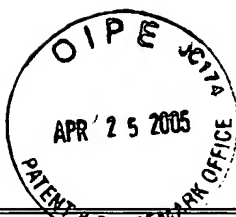
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Respectfully submitted,

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**Please recognize our Customer No. 20277
as our correspondence address.**

INFORMATION DISCLOSURE CITATION IN AN APPLICATION	ATTY. DOCKET NO. 061282-0064	SERIAL NO. 10/787,166
	APPLICANT **Minoru OKAMOTO**	
(PTO-1449)	FILING DATE **February 27, 2004**	GROUP **2825**

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code2 *(if known)*	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US			
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FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Code3 -Number 4 -Kind Codes *(if known)*	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes	No
		WO 01/90882 A2	11/29/2001	**RADIOSCAPE LIMITED**		X	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
		KIM, Tae-Woo., et al. " Hardware Cost Estimation Techniques for C-Level Description." VLSI and CAD, 1999, ICVC, 6th International Conference of Seoul, South Korea, October 26-27, IEEE US, XP010370108, pp. 85-88	
		CARRO, Luigi., et al. "System Synthesis for Multiprocessor Embedded Applications." Design, Automation and Test in Europe, March 27, 2000, XP010377540, pp. 697-702	
		SCIUTO, Donatella., et al. "Metrics for Design Space Exploration of Heterogeneous Multiprocessor Embedded Systems." Proceedings of the 10th International Symposium on Hardware/Software Codesign. Codes, 2002 (IEEE CAT. NO.02TH8627) ACM, New York, NY, May 8, 2002, XP002317354, pp. 55-60	

EXAMINER	DATE CONSIDERED

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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.